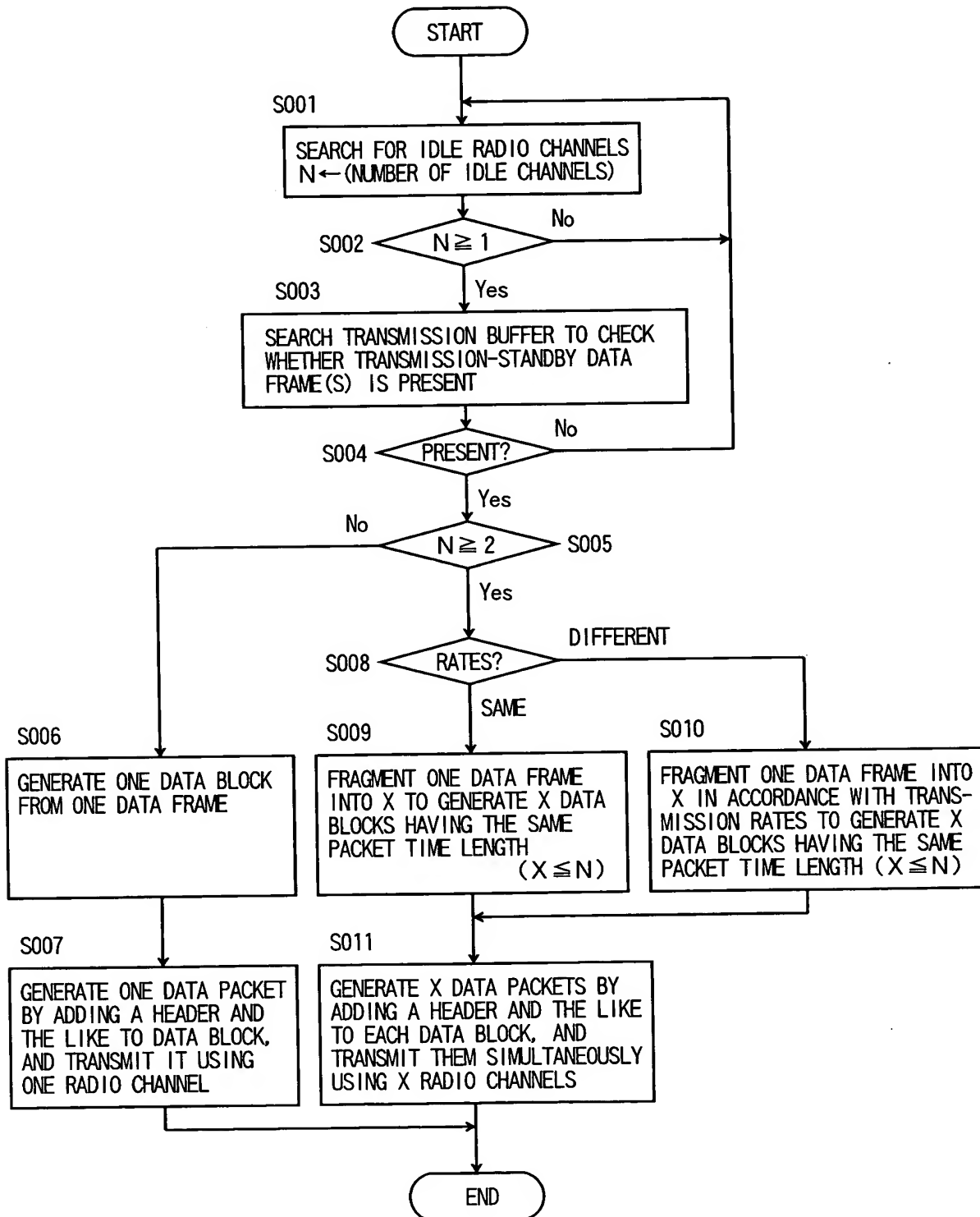


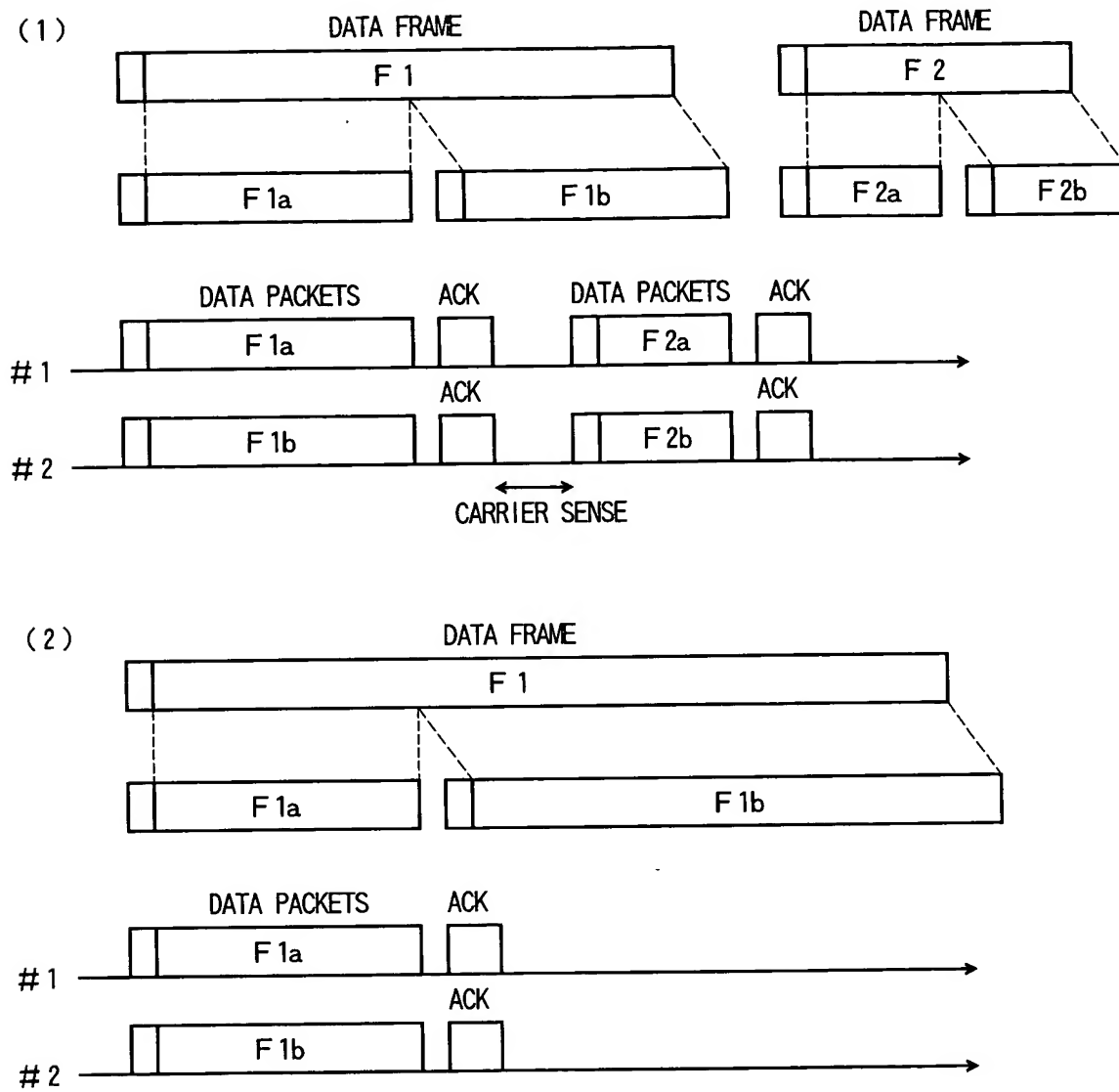
1 / 3 0

FIG. 1



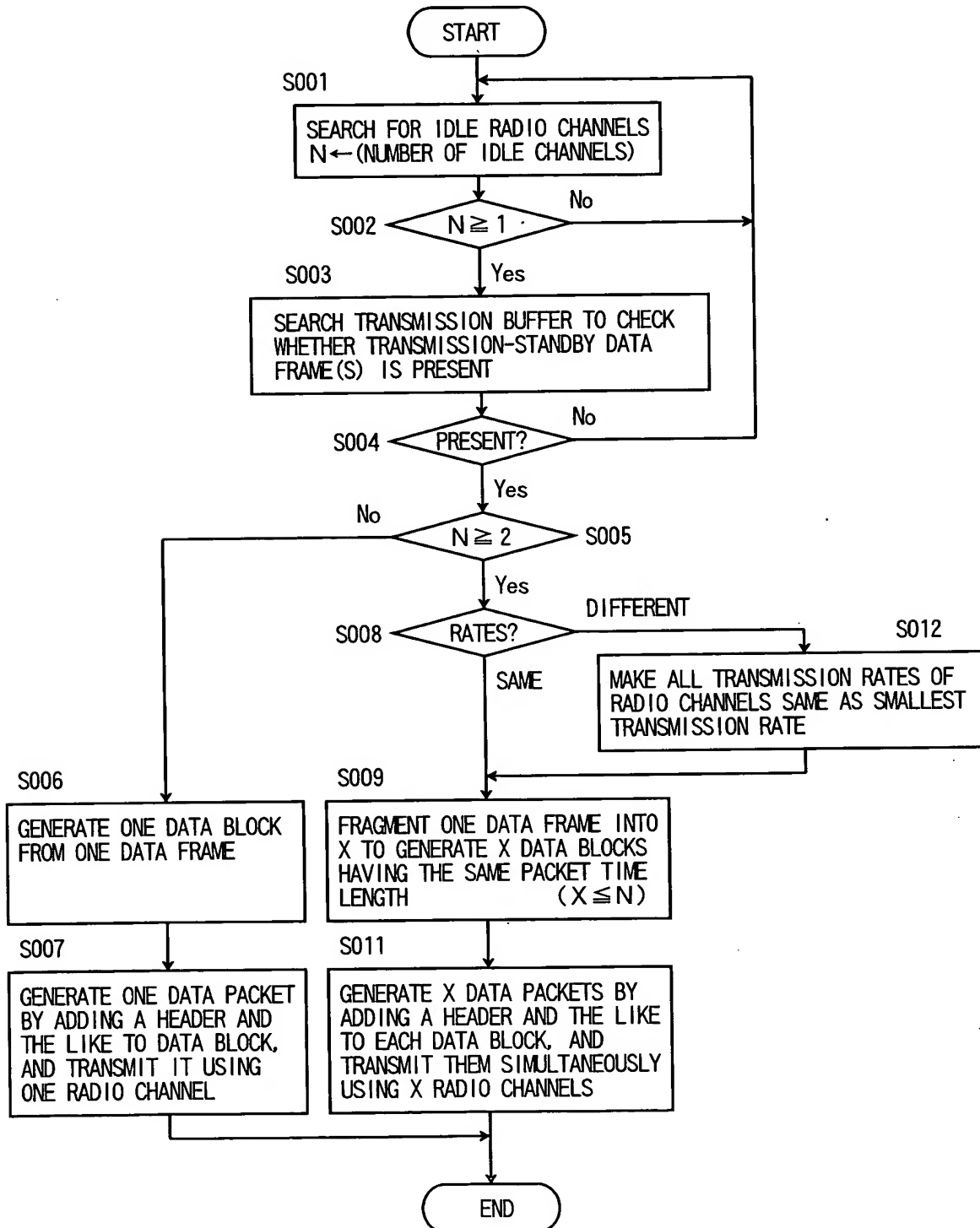
2 / 3 0

FIG. 2



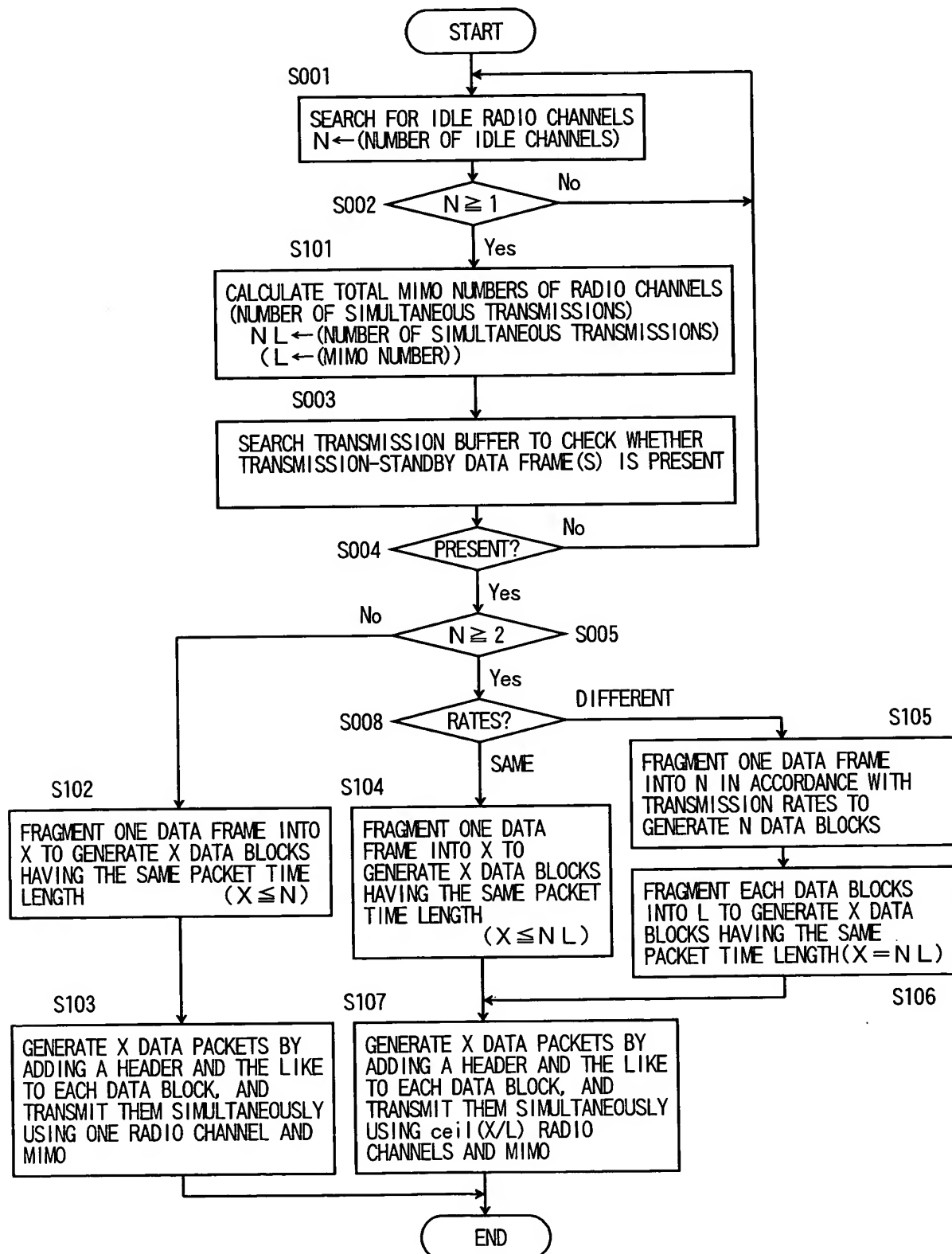
3 / 3 0

FIG. 3



4 / 3 0

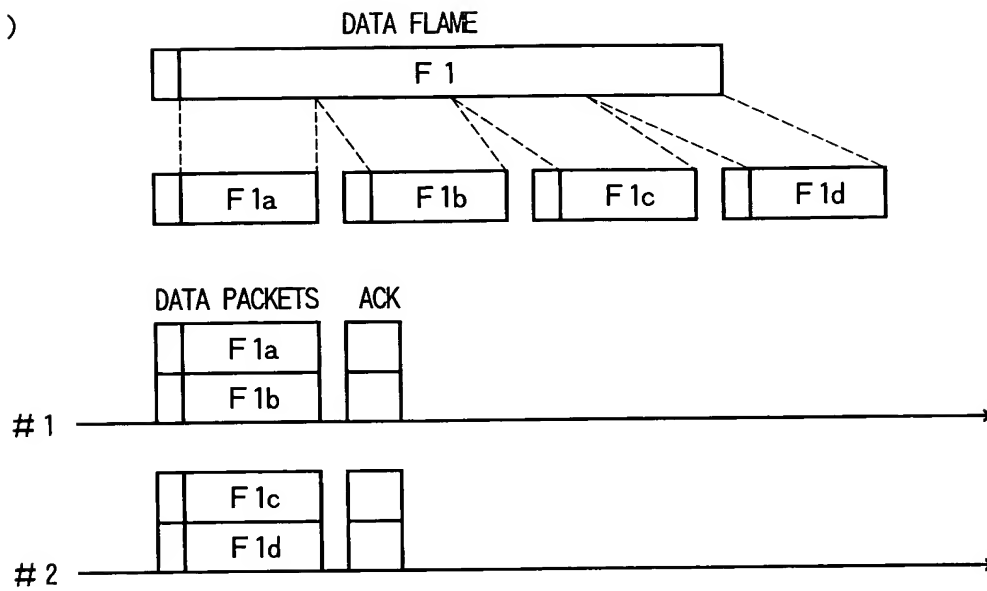
FIG. 4



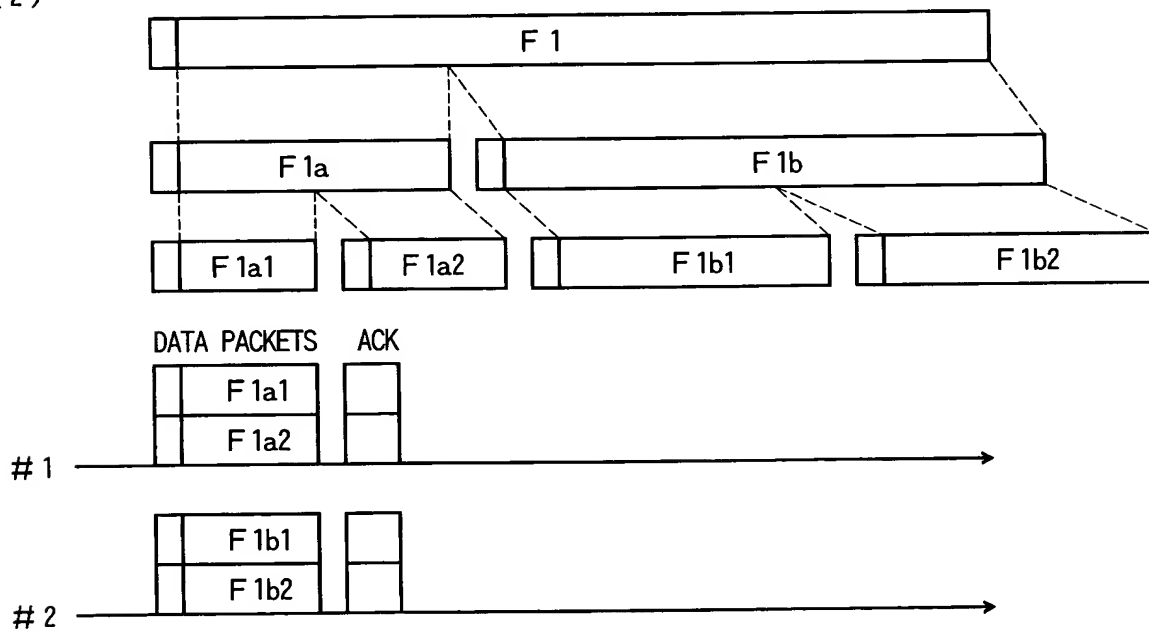
5 / 3 0

FIG. 5

(1)



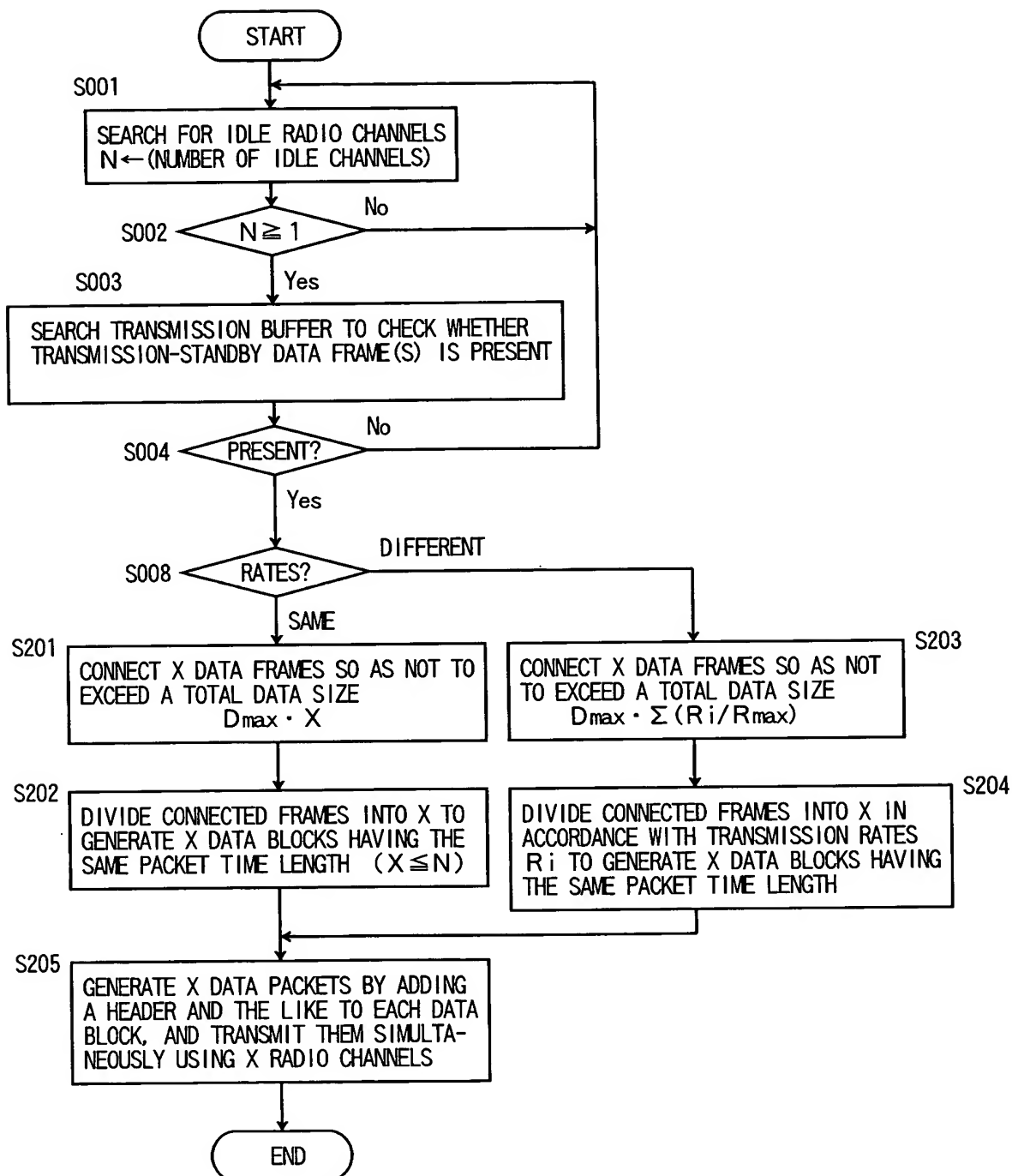
(2)



10/542209

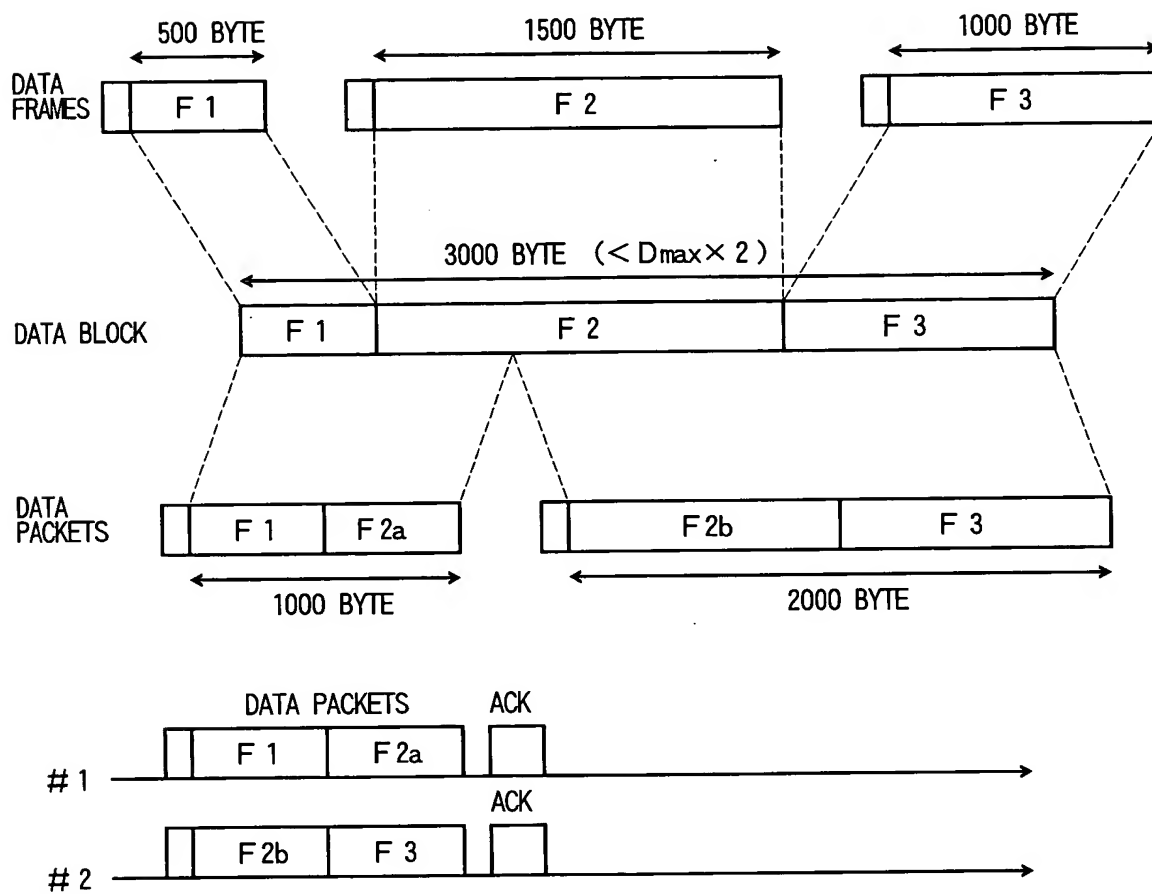
6 / 3 0

FIG. 6



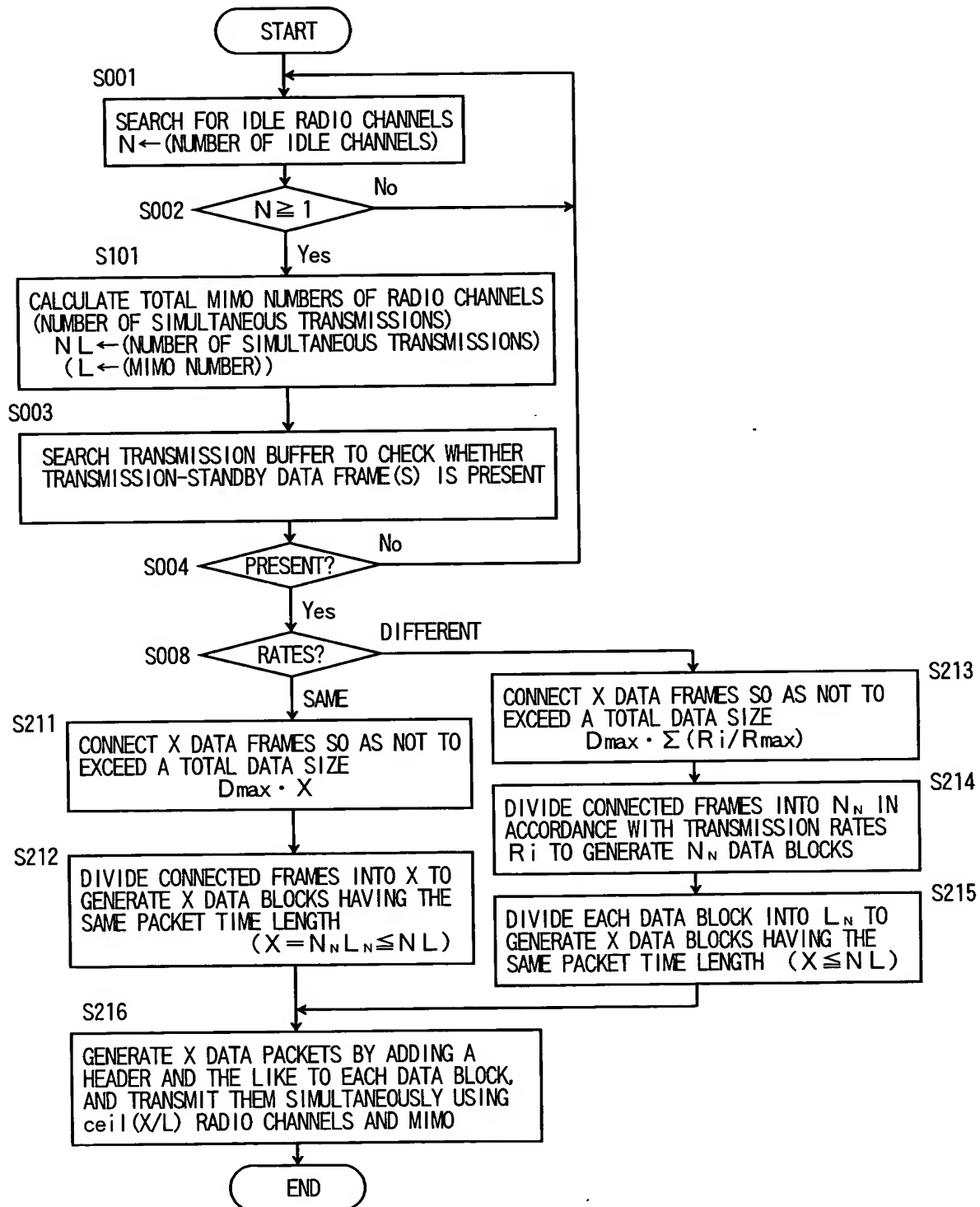
7/30

FIG. 7



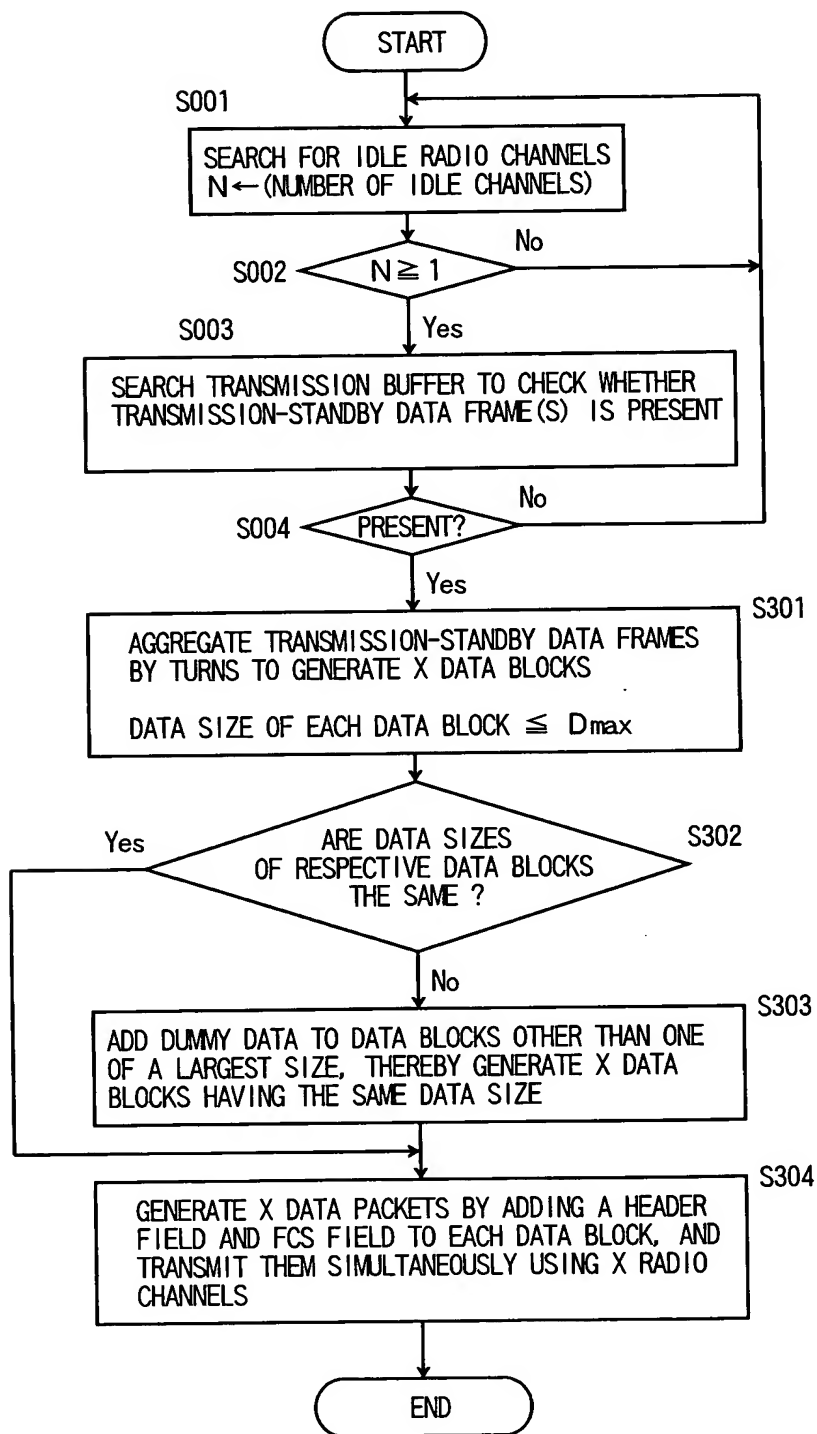
8 / 30

FIG. 8



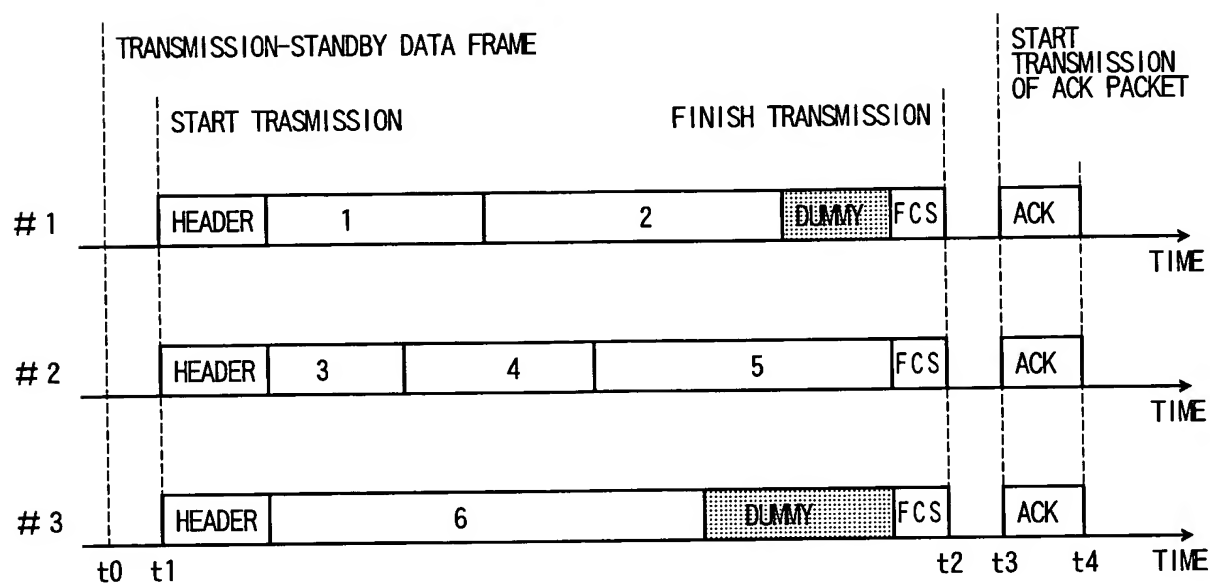
9 / 3 0

FIG. 9



10/30

FIG. 10

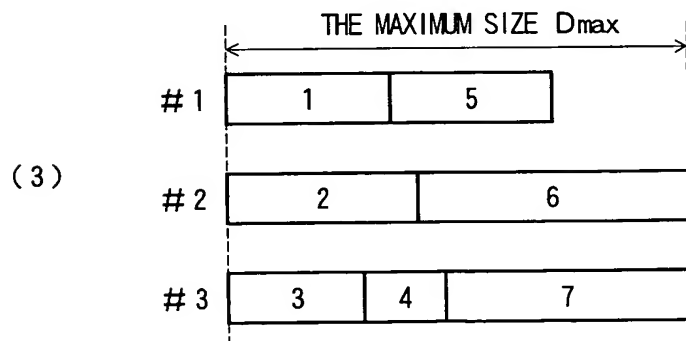
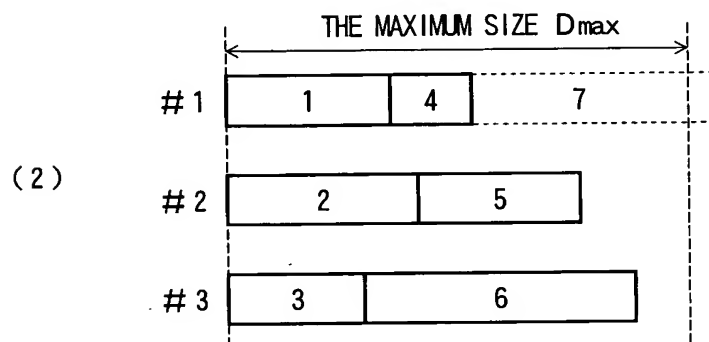
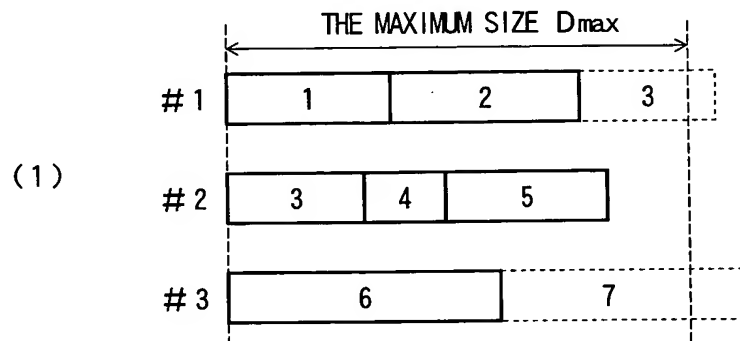


11/30

FIG. 11

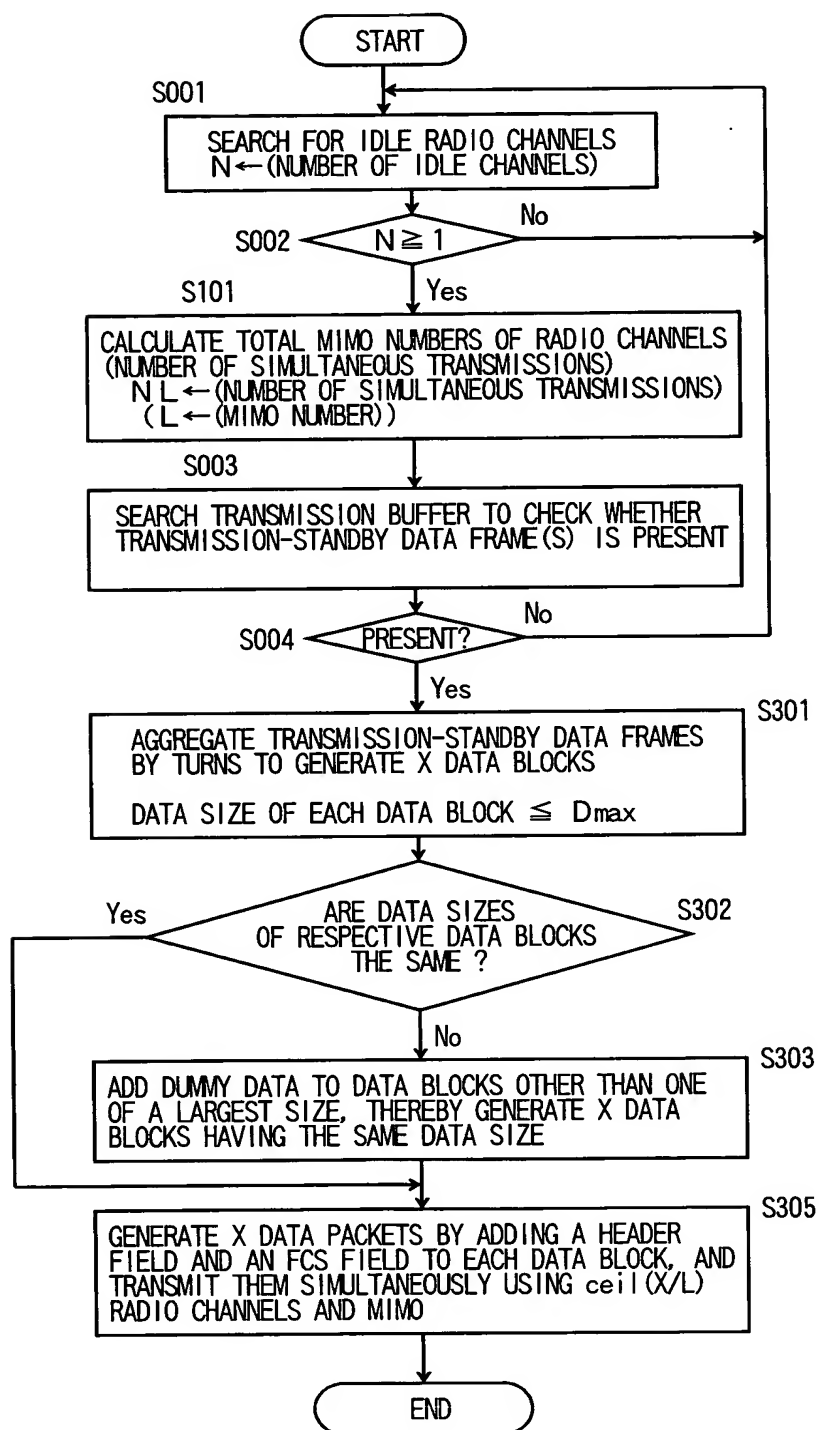
DATA FRAME ON TRANSMISSION BUFFER

1	2	3	4	5	6	7
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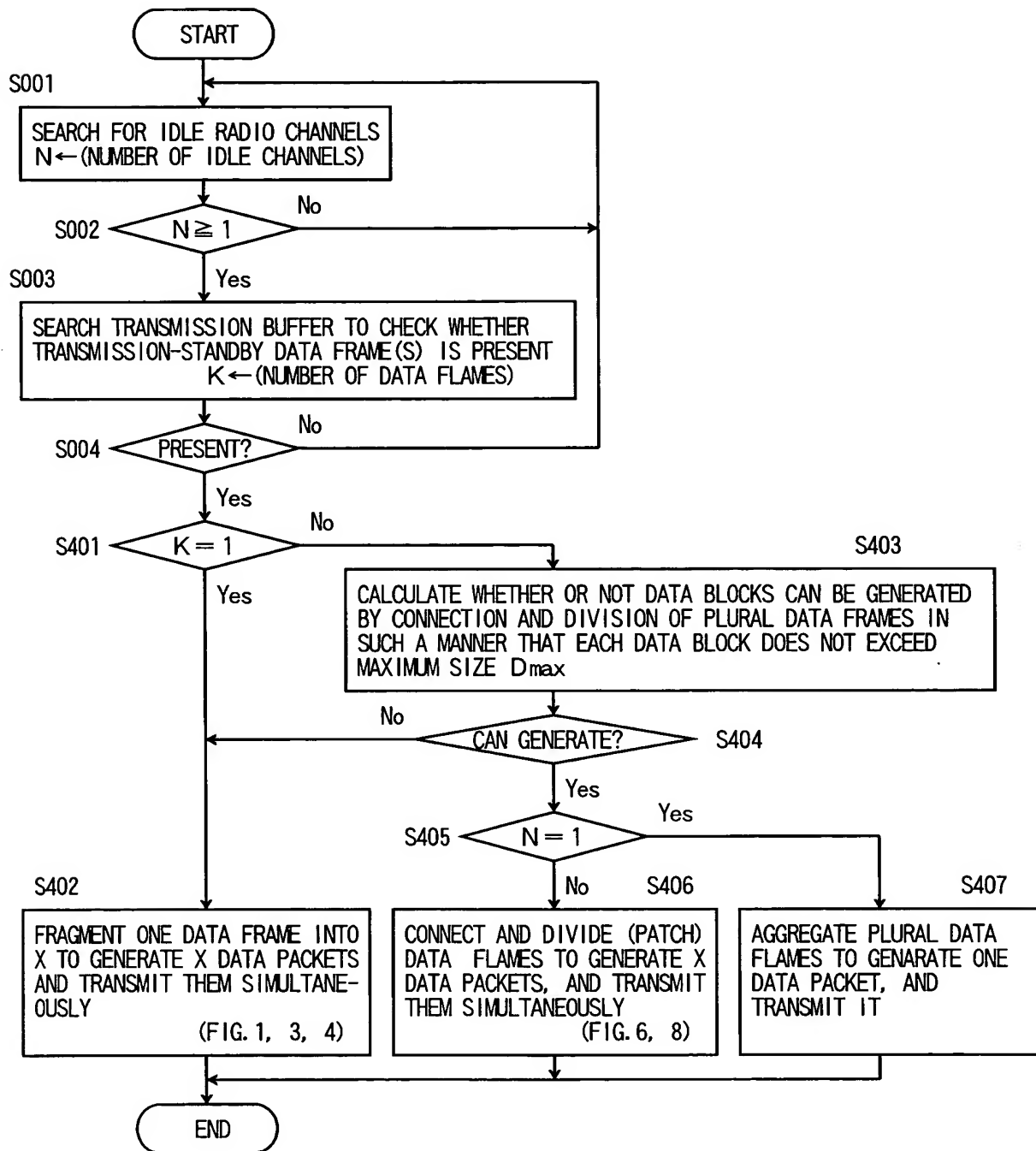
12/30

FIG. 12



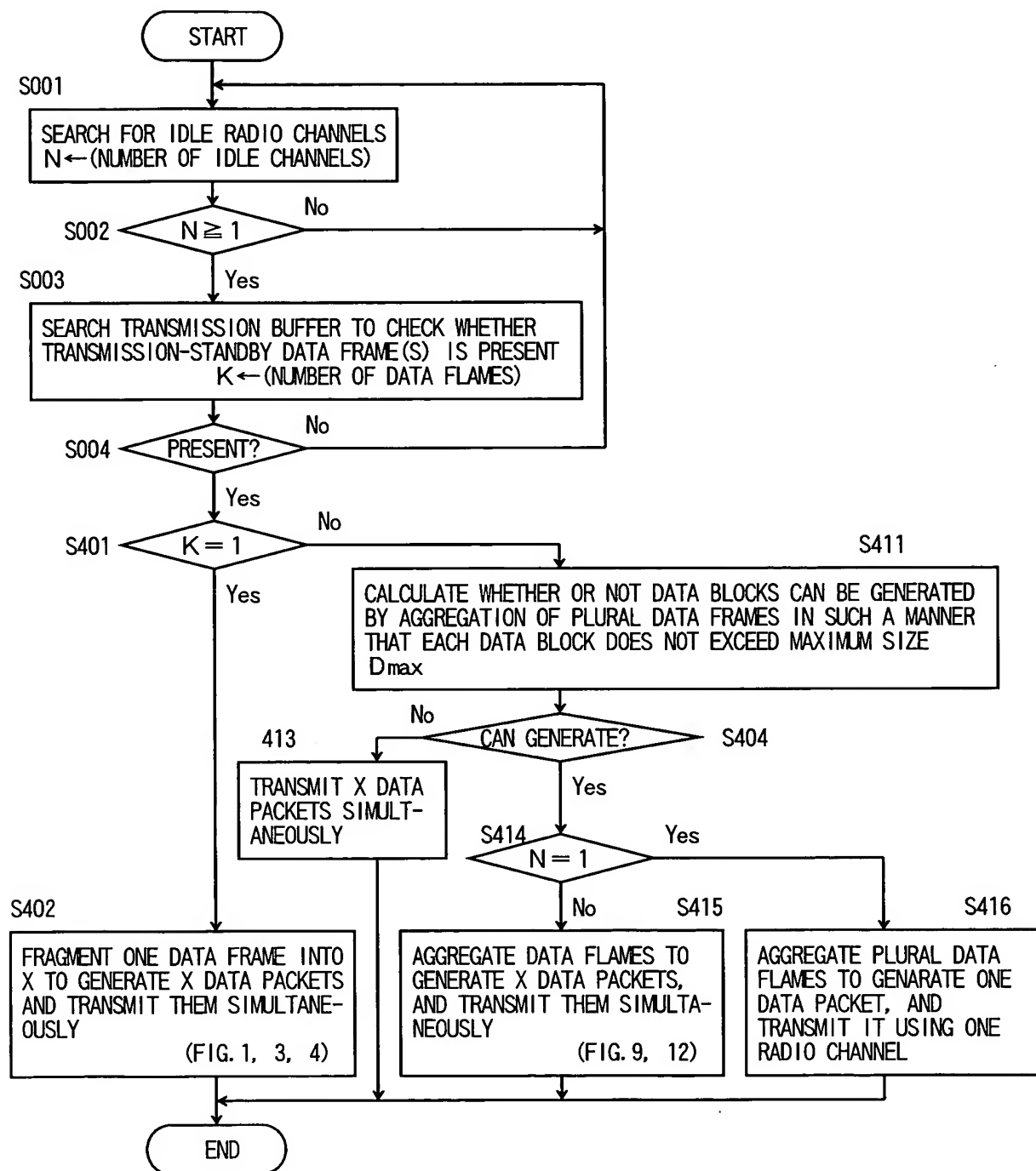
13/30

FIG. 13



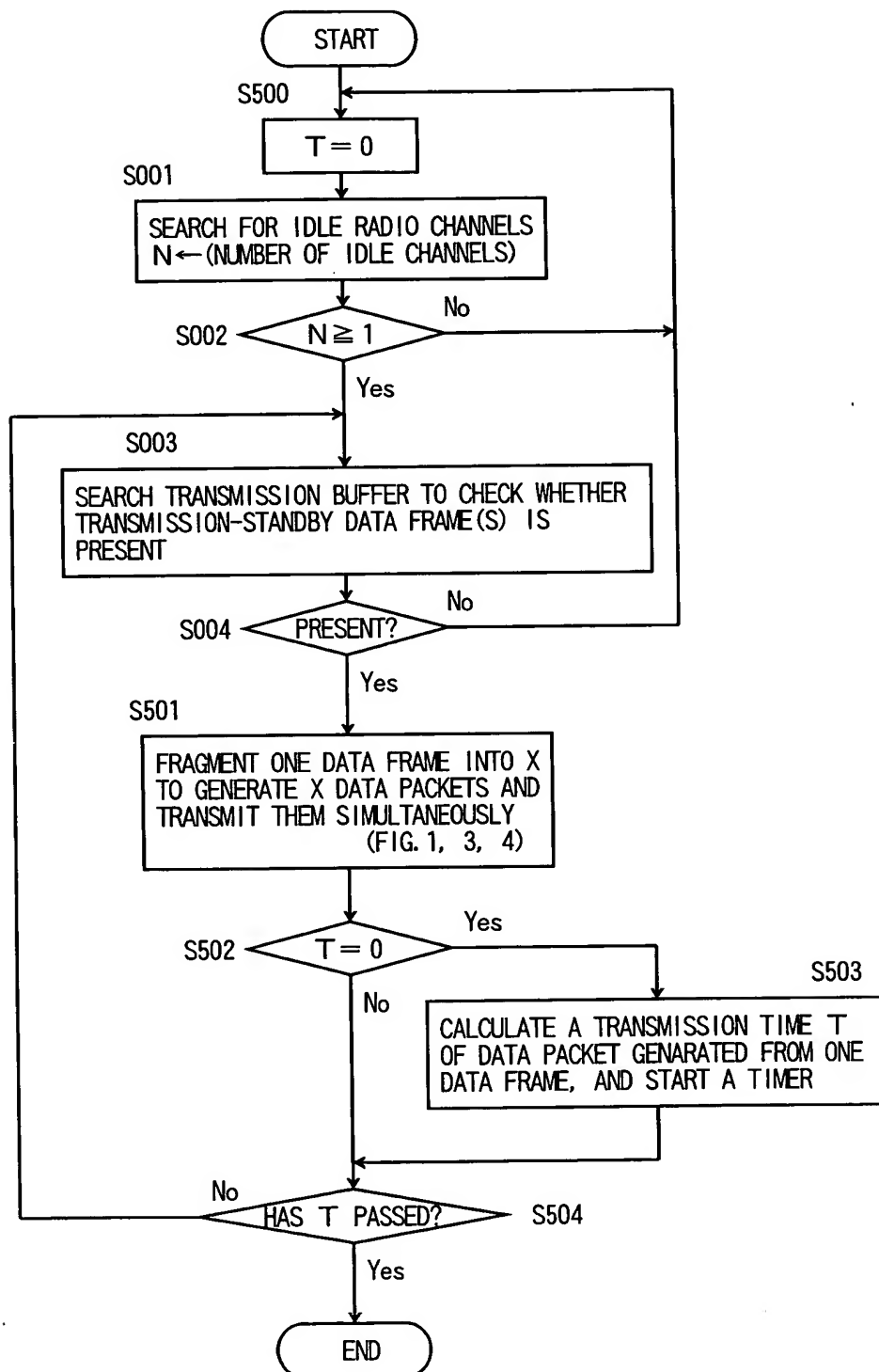
14/30

FIG. 14



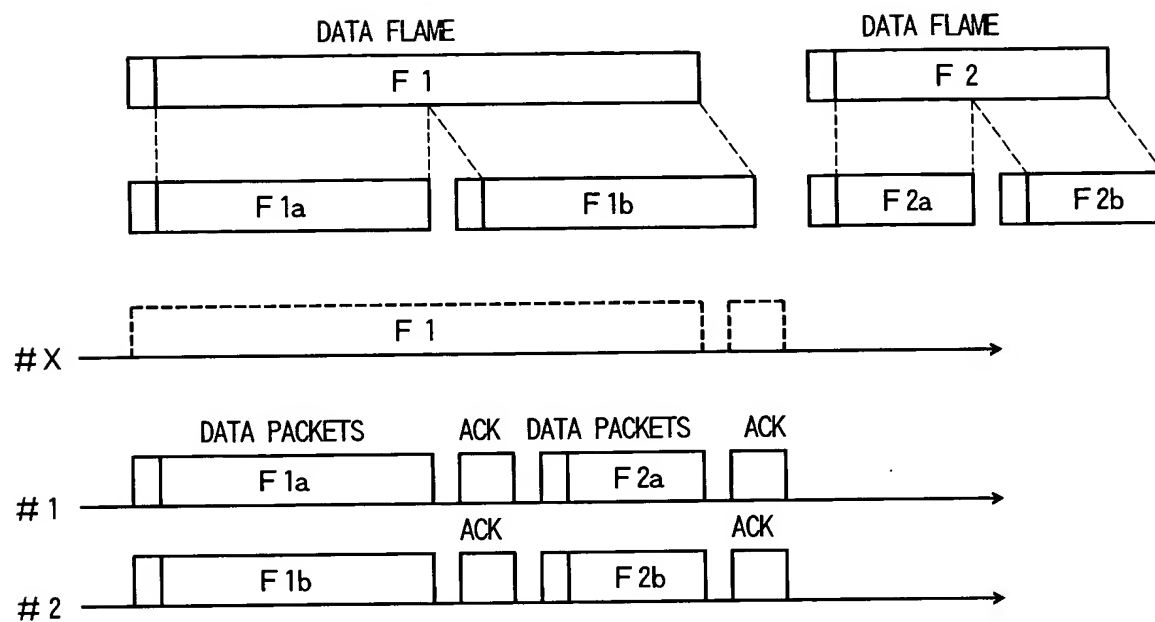
15 / 30

FIG. 15



16/30

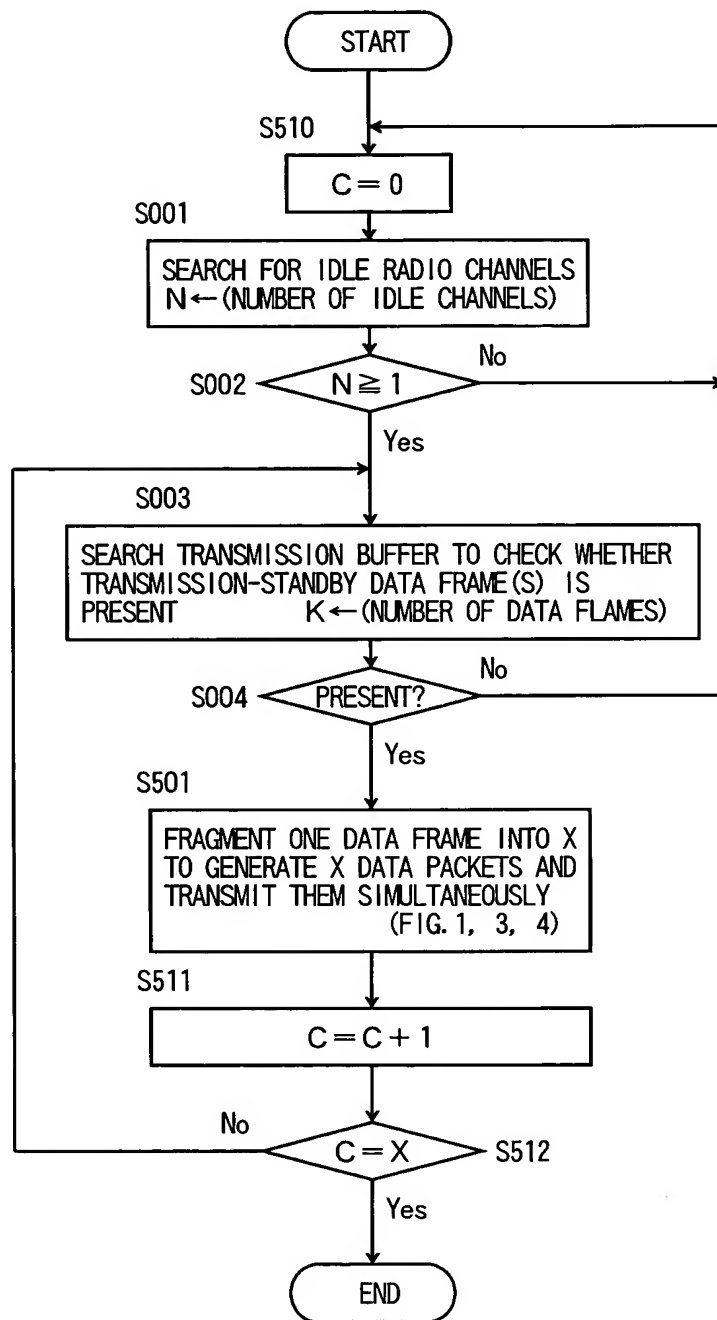
FIG. 16



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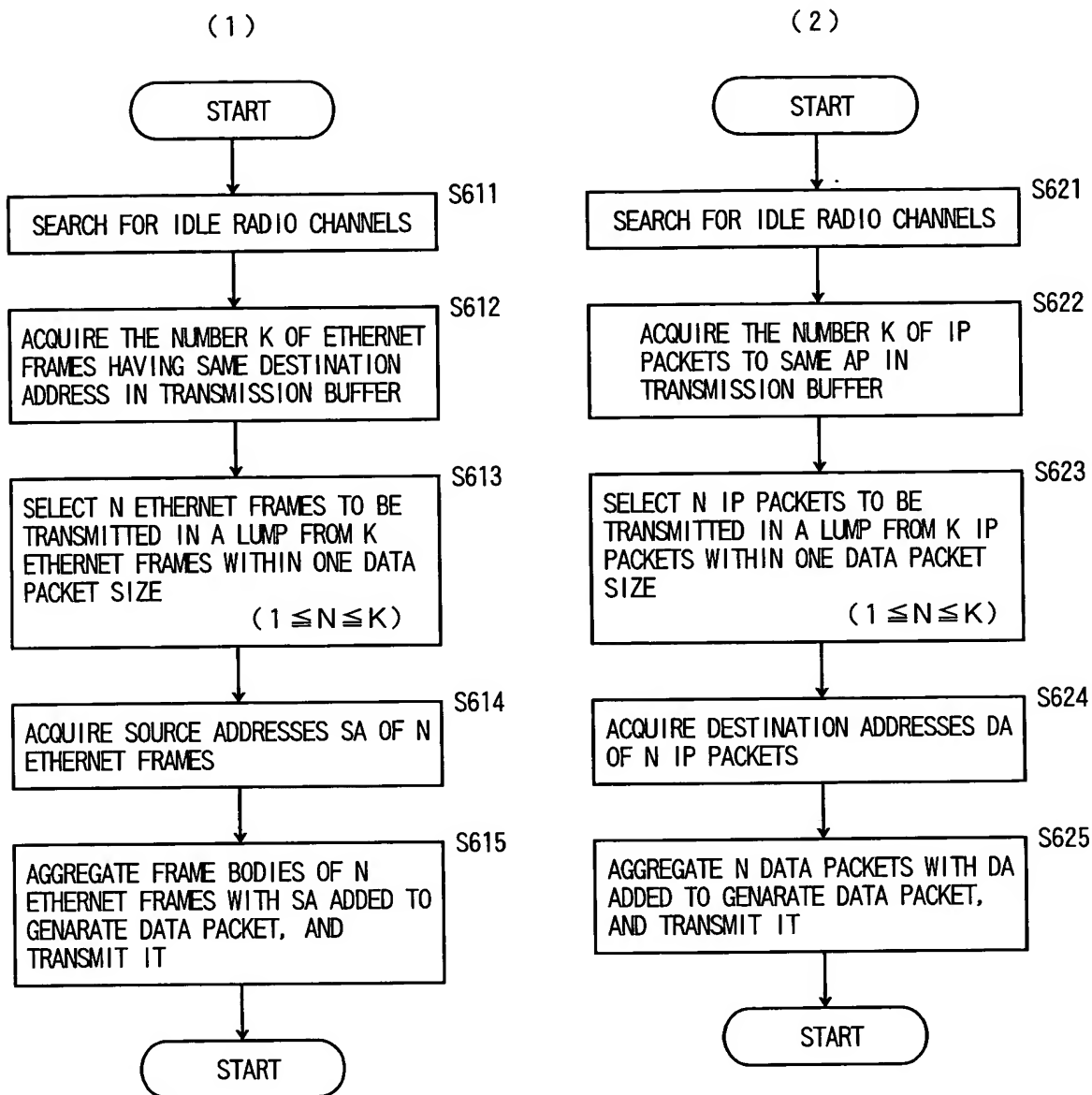
17/30

FIG. 17



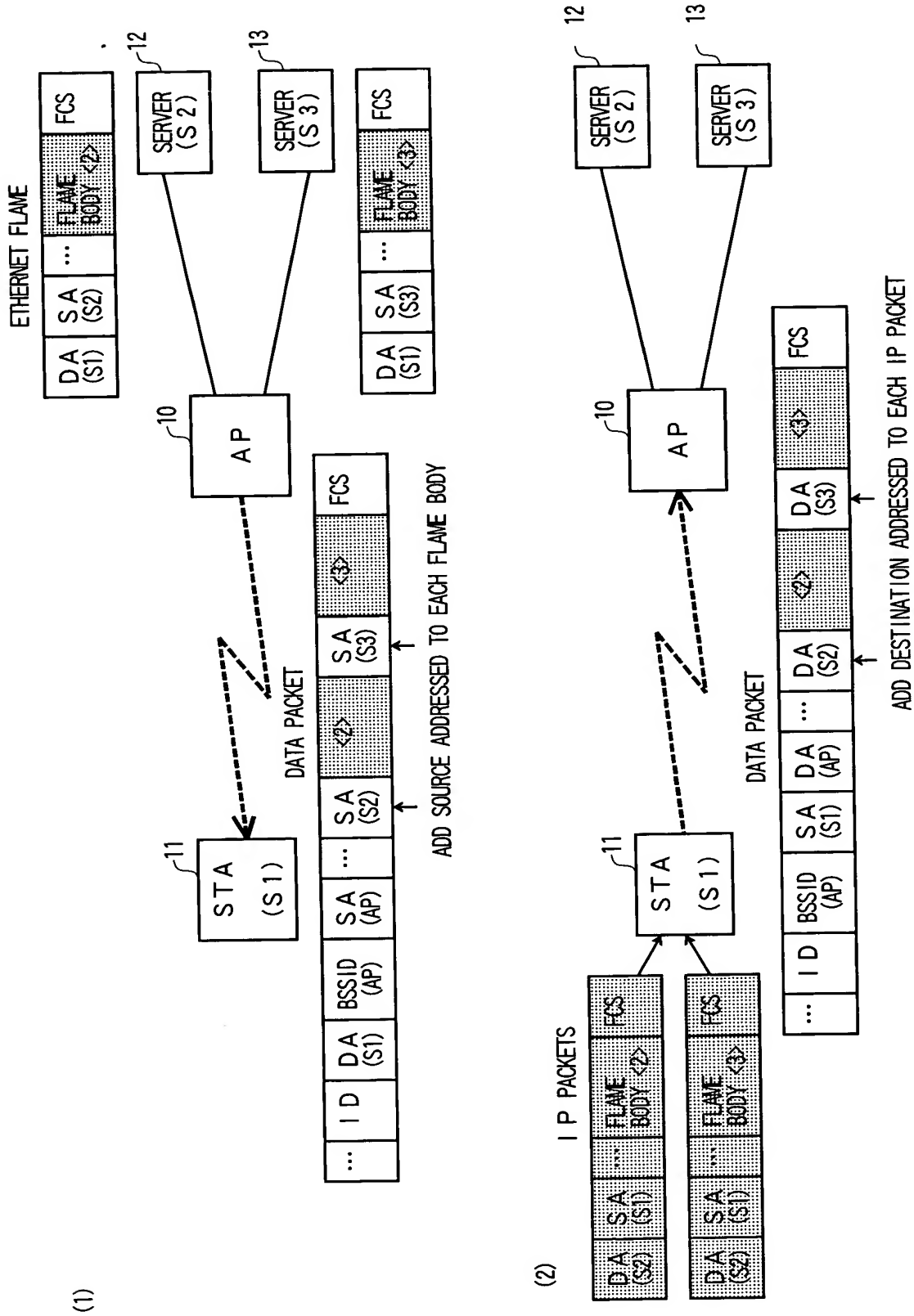
18/30

FIG. 18



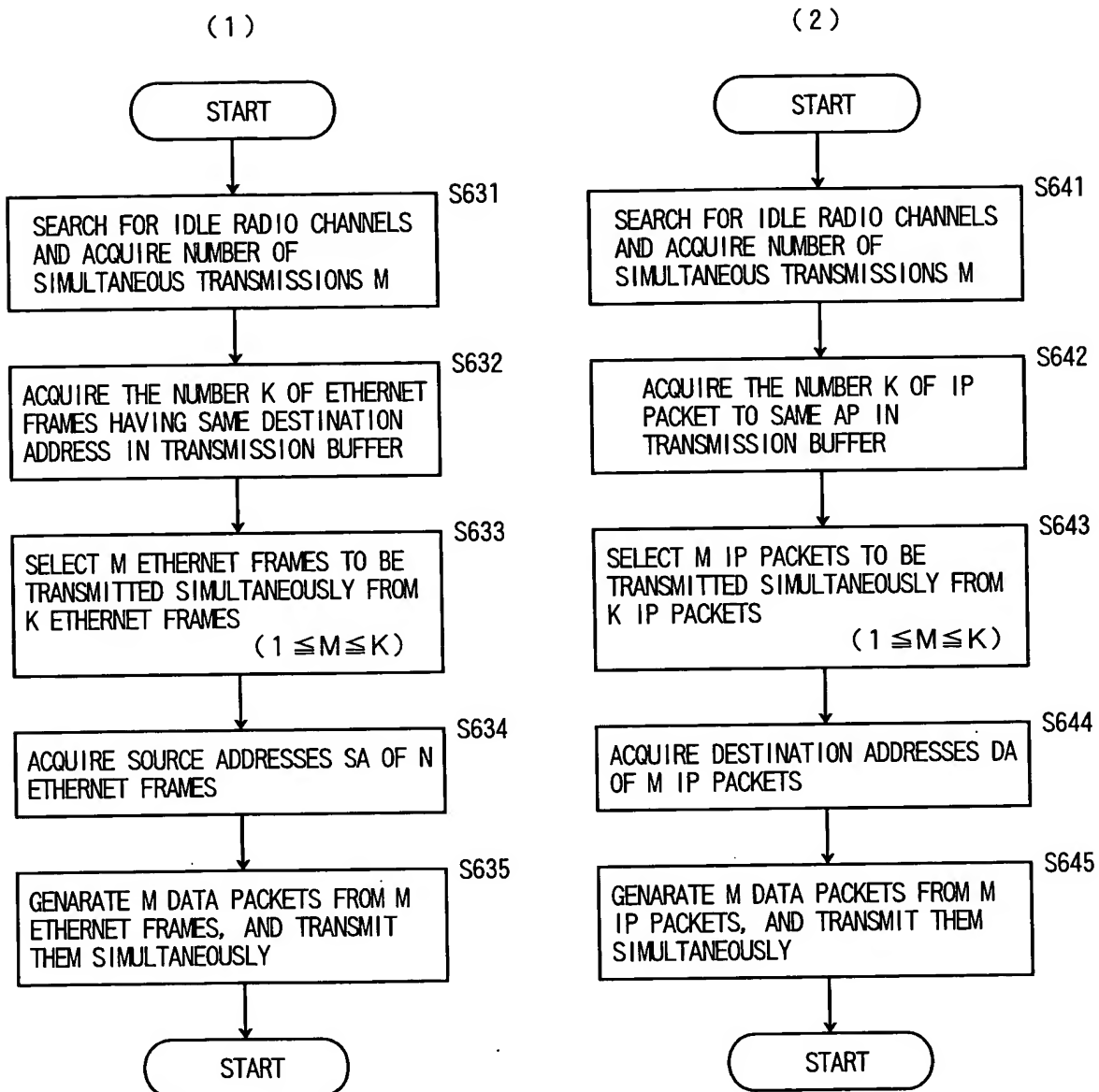
19/30

FIG. 19



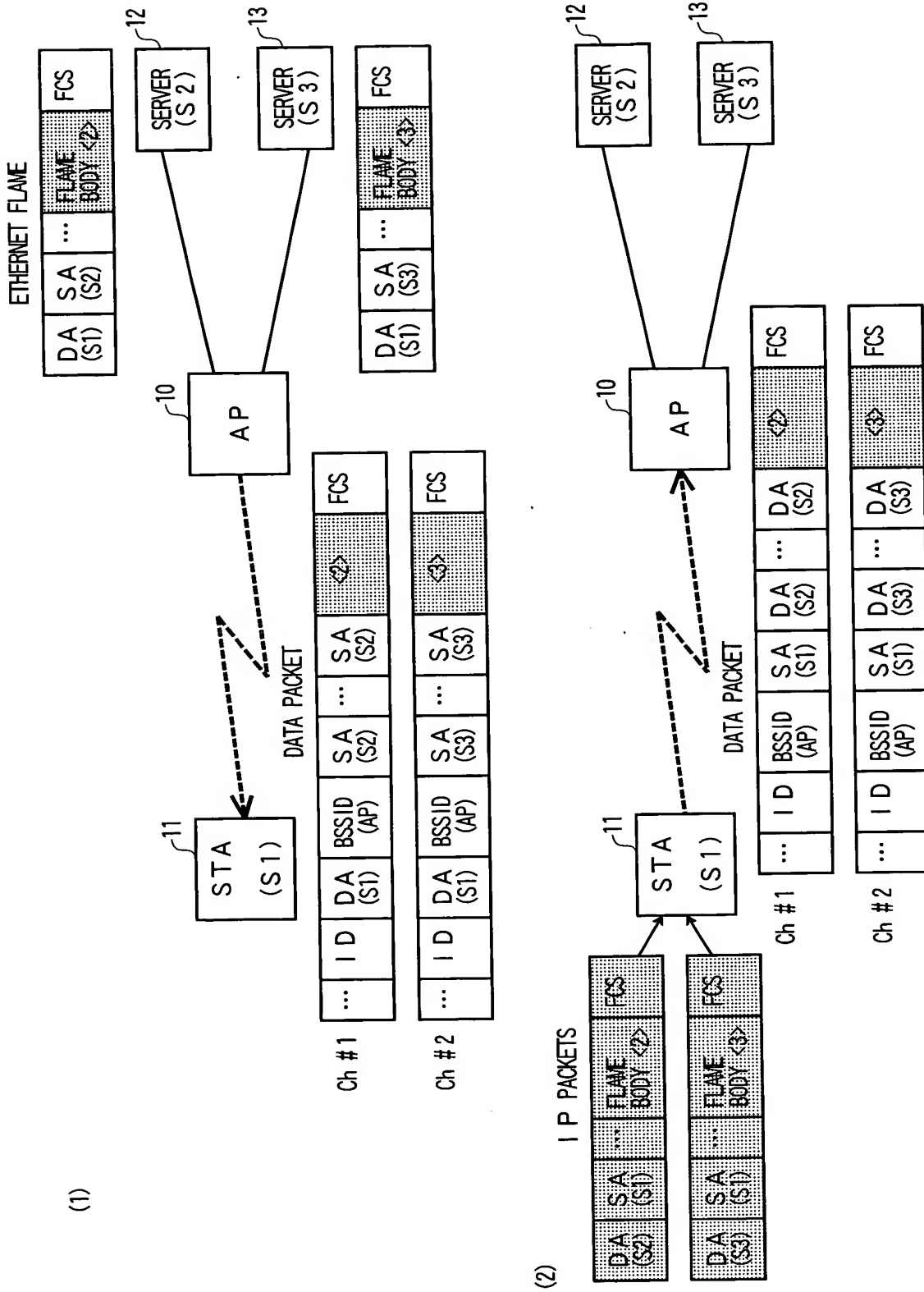
20/30

FIG. 20



21 / 30

FIG. 2 1



22 / 30

FIG. 22

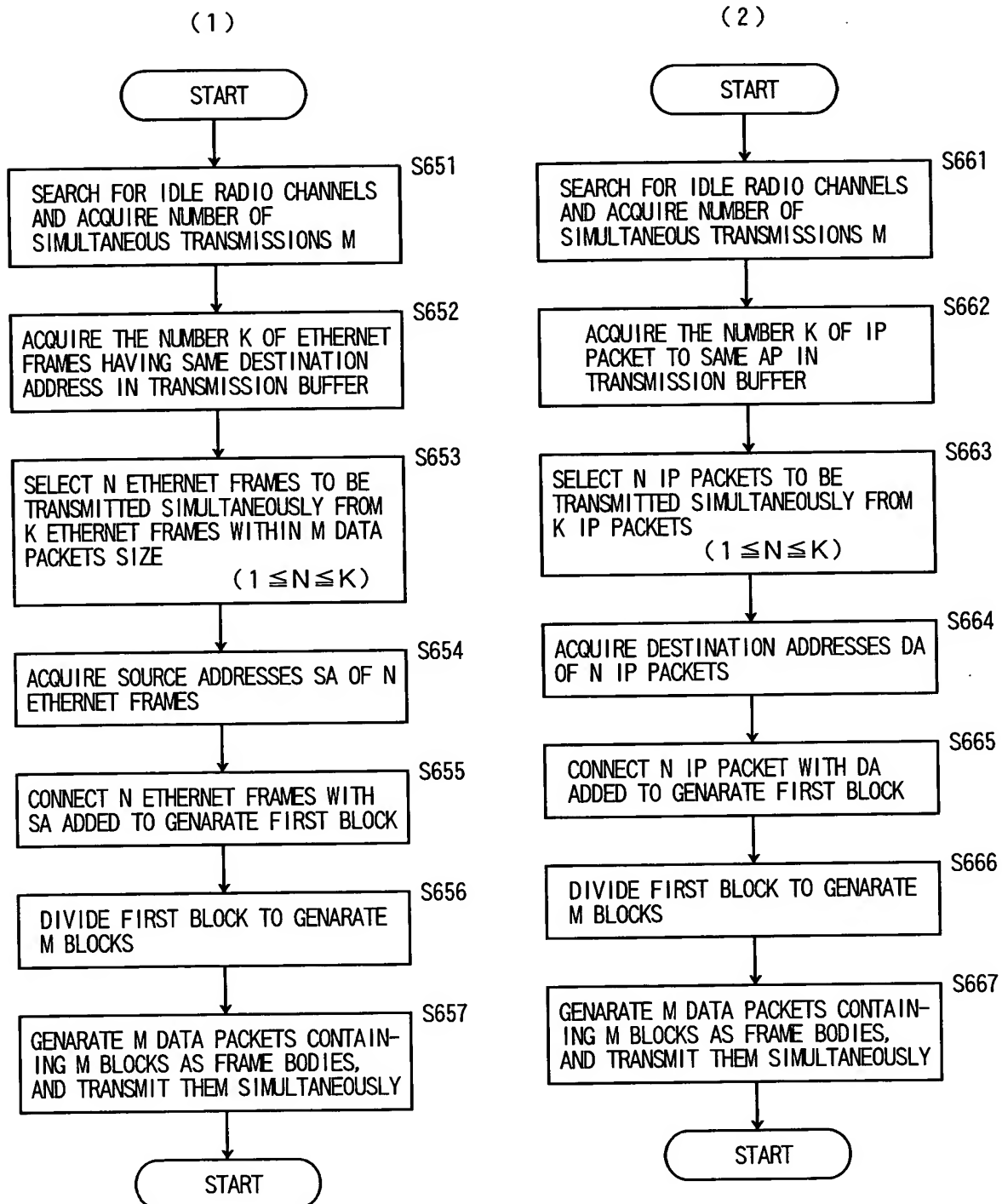
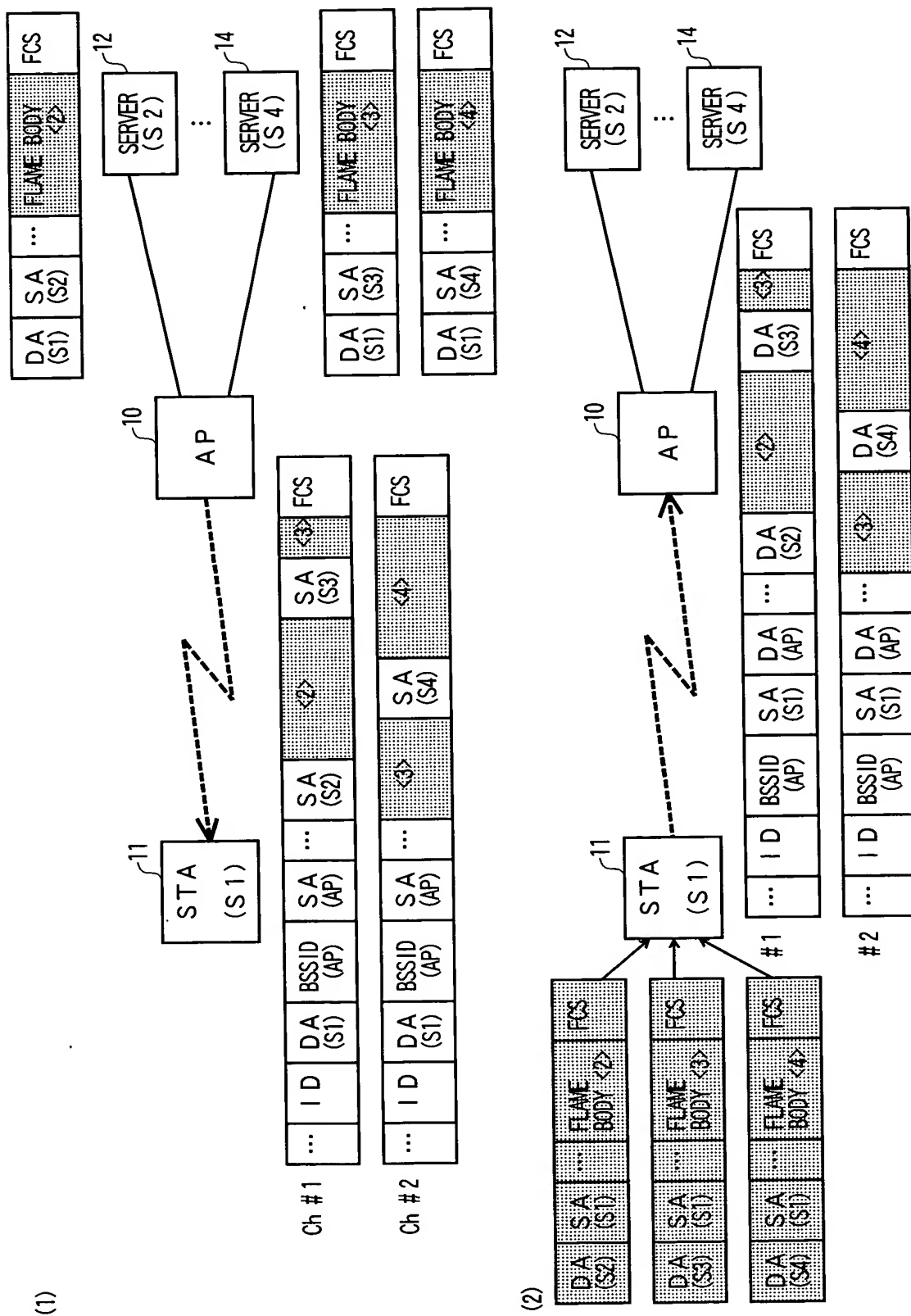


FIG. 23



(1)

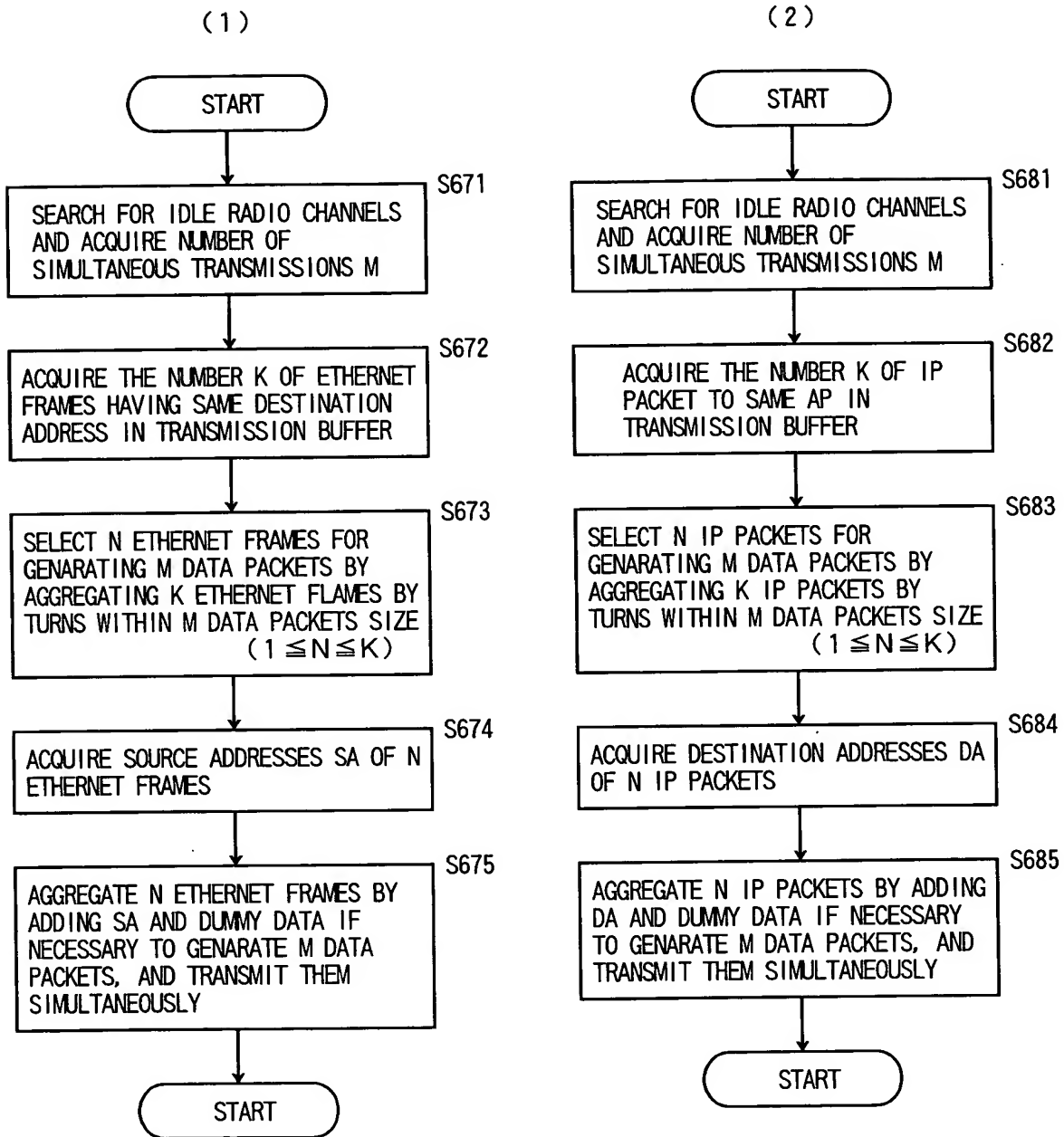
(2)

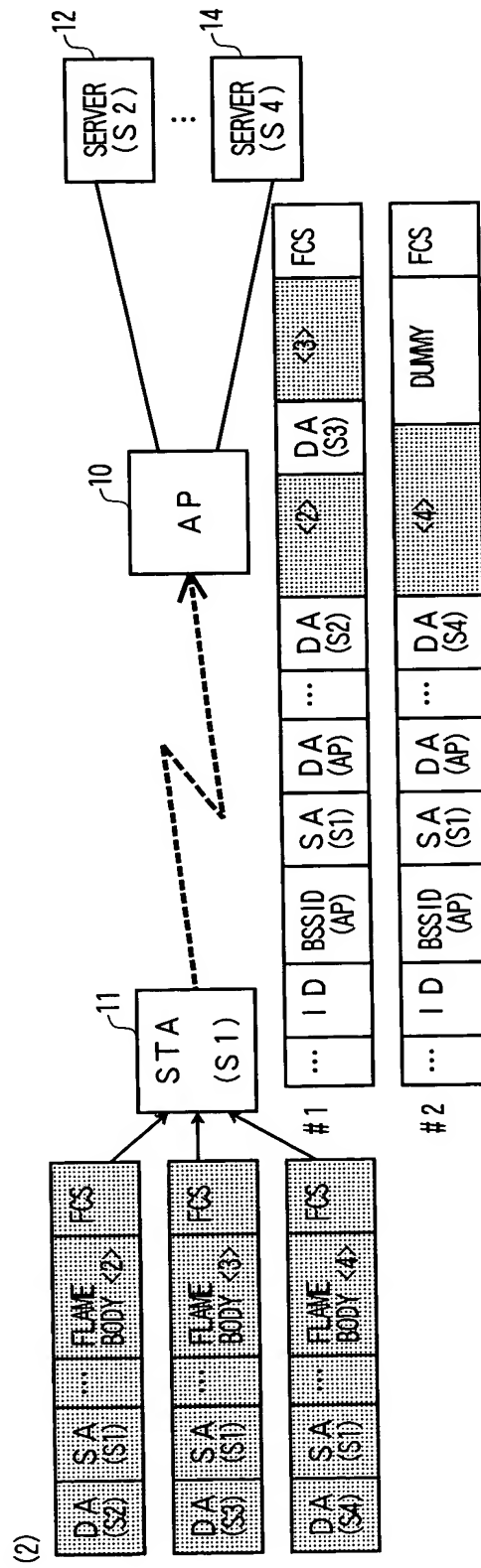
#2

#2

24/30

FIG. 24

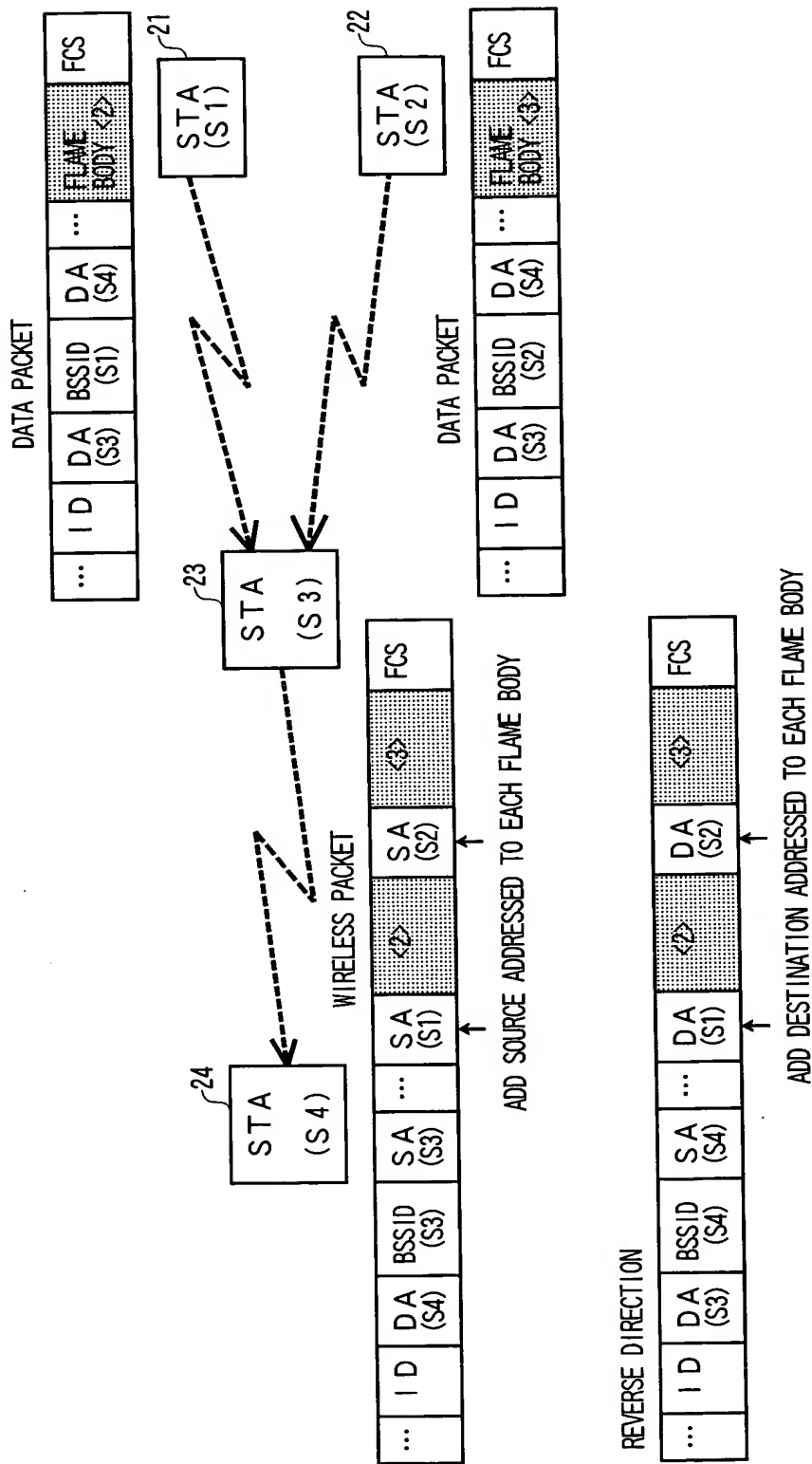




10/542209

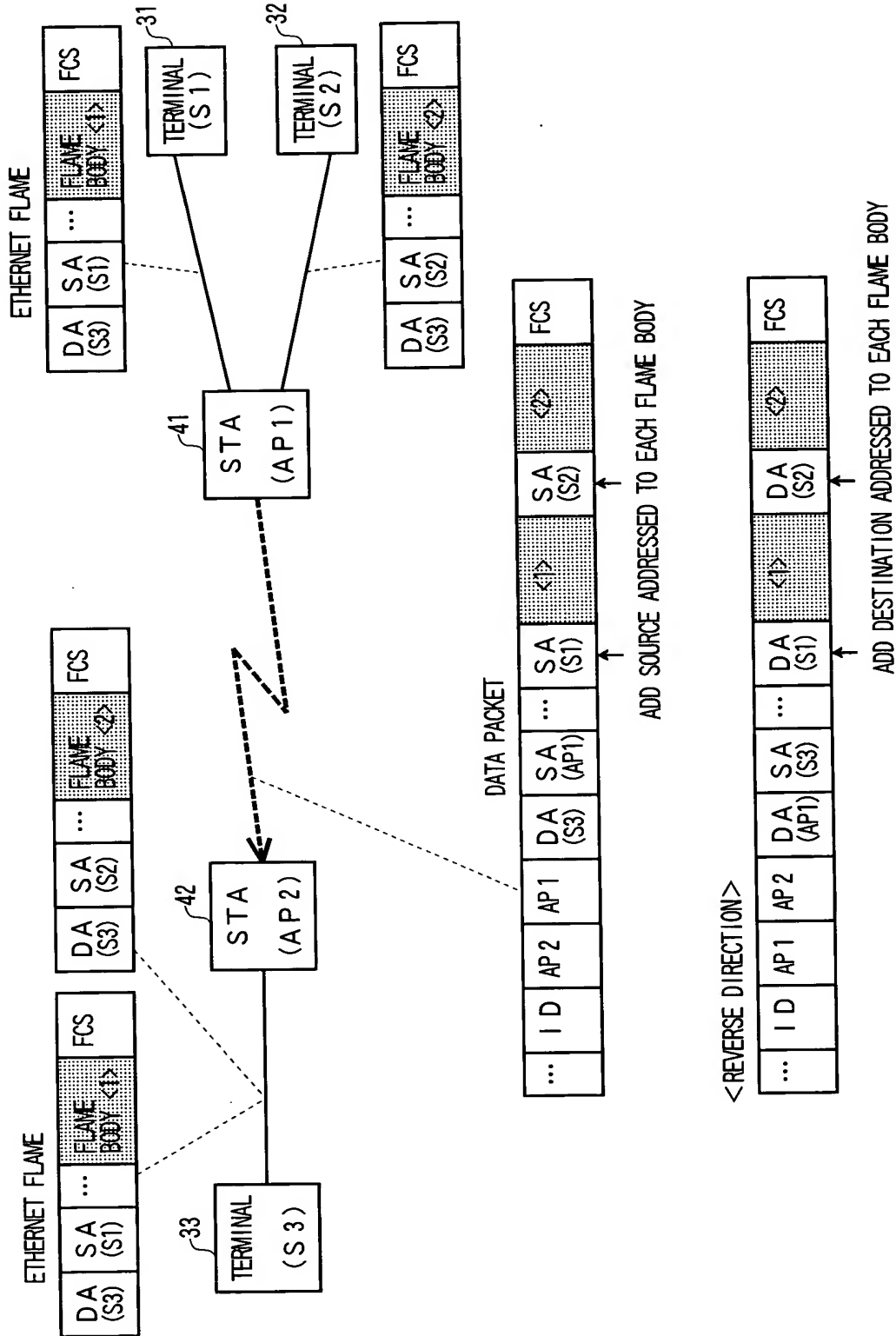
26 / 30

FIG. 26



27 / 30

FIG. 27



28/30

FIG. 28

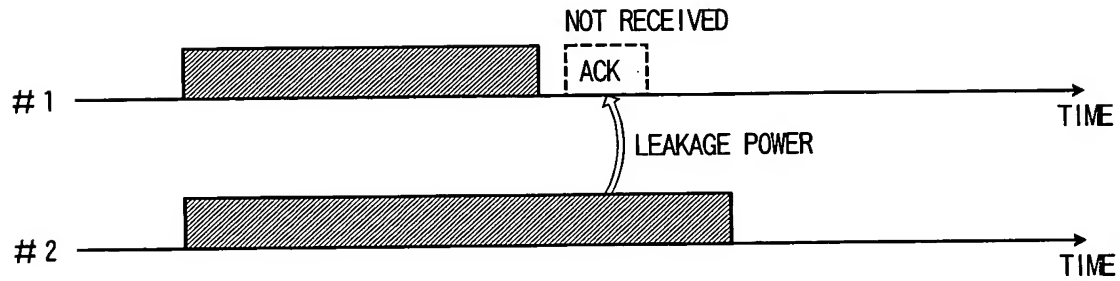


FIG. 29

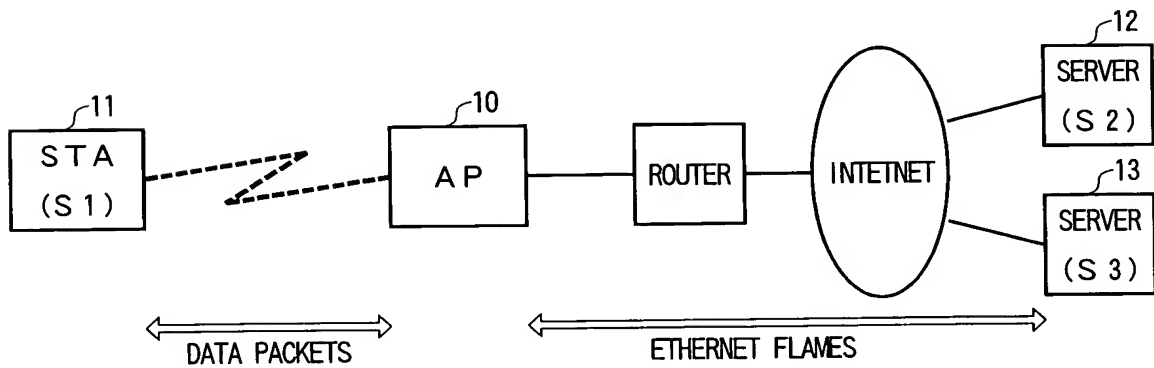
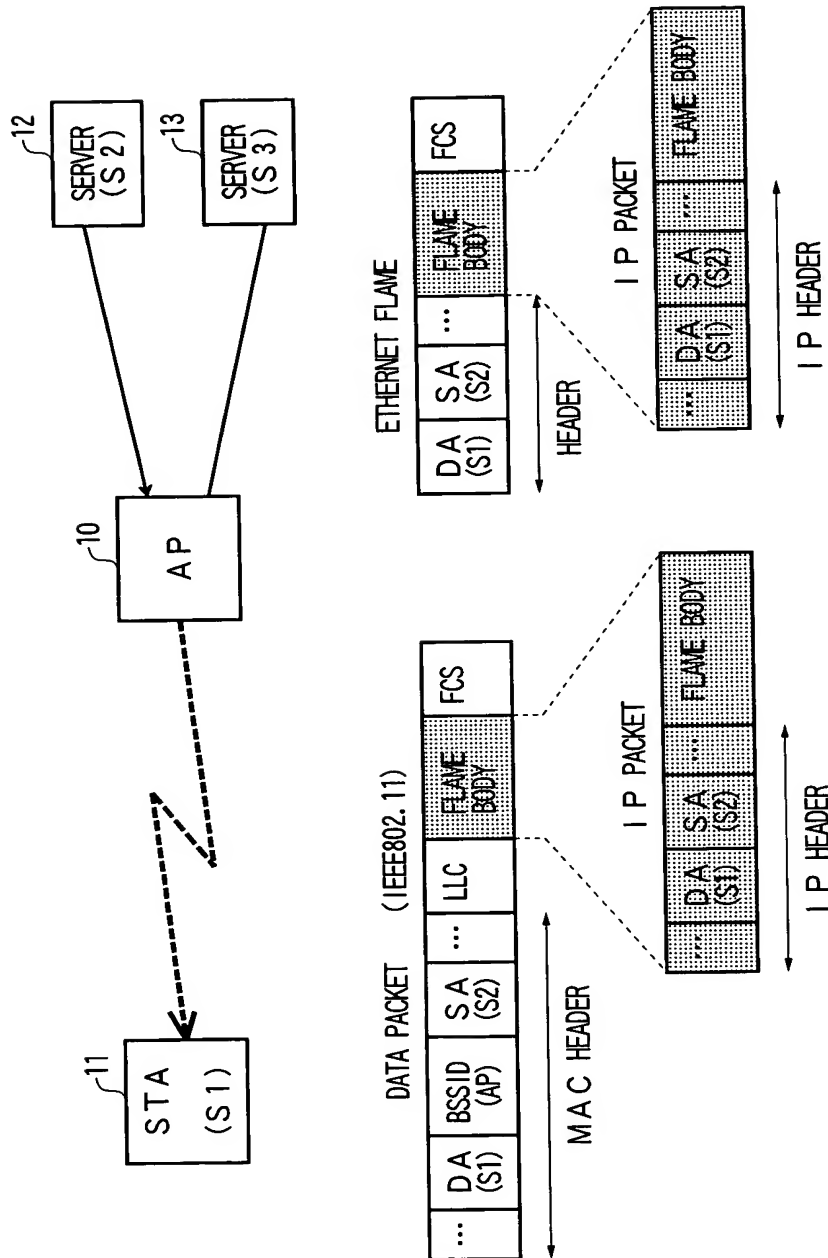


FIG. 30



10/542209

30/30

FIG. 3 1

